

Stephan Wong

Stephan Wong received his PhD degree in 2002 from the Electrical Engineering Department at Delft University of Technology (TU Delft), The Netherlands. He is currently working as an assistant professor in the Computer Engineering Laboratory at TU Delft. He has considerable experience in the design of embedded media processors. He has also worked on microcoded FPGA complex instruction engines and the modeling of parallel processor communication networks. His research interests include embedded systems, multimedia processors, complex instruction set architectures, reconfigurable and parallel/distributed processing, microcoded machines, and network processors. He is a member of the IEEE, the IEEE Computer Society, and ACM.